

Claims

- [c1] What is claimed is:
- 1.A computer algorithm for converting a text file into a circuit design database and vice versa so that a user can update the text file and the corresponding circuit design database;
 - the computer algorithm comprising:
 - compiling a hardware description written in the text file into the corresponding circuit design database;
 - converting comment text written in the text file into a code stream according to a predetermined conversion rule;
 - updating the circuit design database and compiling the updated circuit design database into the corresponding hardware description; and
 - converting the code stream into the comment text, and inserting the comment text into the hardware description corresponding to the updated circuit design database so as to generate an updated text file.
- [c2] 2.The computer algorithm of claim 1 wherein a format of the comment text is different from a format of the code stream after being converted according to the predetermined conversion rule.
- [c3] 3.The computer algorithm of claim 1 wherein the text file conforms to a specification of a Verilog hardware description language.
- [c4] 4.The computer algorithm of claim 3 further comprising applying an electronic design aided tool (EDA) to compile the hardware description into the corresponding circuit design database, and updating the circuit design database through the electronic design aided tool.
- [c5] 5.The computer algorithm of claim 1 wherein the code stream comprises space codes, tab codes, or enter codes.
- [c6] 6.The computer algorithm of claim 1 further comprising:
 - generating a header attached before the code stream for indicating a starting point of the code stream.
- [c7] 7.The computer algorithm of claim 6 wherein the header comprises space

codes, tab codes, or enter codes.

- [c8] 8.The computer algorithm of claim 1 wherein the code stream is stored in a data container.
- [c9] 9.The computer algorithm of claim 8 wherein the data container is stored in a memory.
- [c10] The computer algorithm of claim 8 wherein the data container is stored in a hard disk drive.